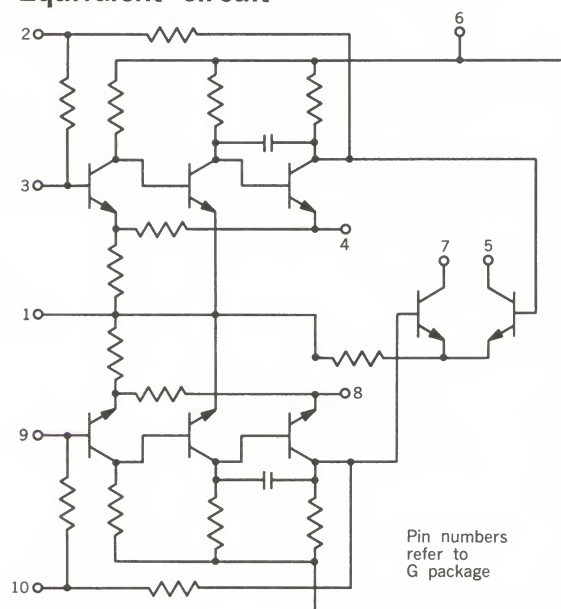


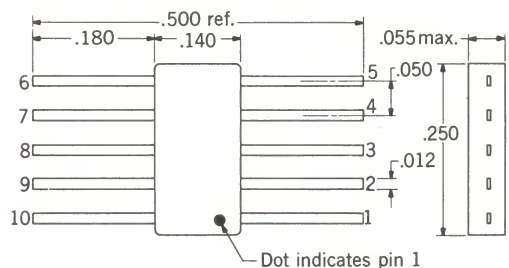


Equivalent circuit



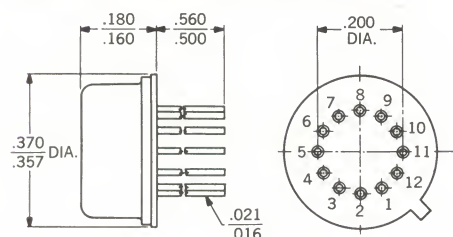
Package

G style FLAT-PAK (EIA TO-89)



- | | |
|-------------------|-------------------|
| 1. Common | 6. V_{CC} |
| 2. Bypass | 7. Output |
| 3. Input | 8. Volume control |
| 4. Volume control | 9. Input |
| 5. Output | 10. Bypass |

T style (EIA Registration Pending)



- | | |
|-------------------|-------------------|
| 1. Common | 7. V_{CC} |
| 2. Bypass | 8. Output |
| 3. Input | 9. Volume control |
| 4. Volume control | 10. Input |
| 5. Output | 11. Bypass |
| 6. No connection | 12. No connection |

General description

The WC 183 general purpose low level audio amplifier consists of an eight transistor balanced circuit with internal DC feedback fabricated as one monolithic silicon chip. A three stage class A preamplifier gives high gain, and is followed by a class B output stage to provide high overall efficiency. The unit has a very low quiescent current drain and provides excellent performance on high gain, low power requirements. A unique feature is the ability of the amplifier to provide high gain, using only a single one cell battery for power.

Application

This amplifier is suggested for radio, recording, and other audio applications in the milliwatt range. The use of an optional rolloff capacitor makes it ideal for audio use in voice communications equipment where limited frequency response is desired. For higher fidelity audio applications, a simple feedback network extends the flat frequency response to well beyond the audio range. Its input and output connections and impedances are compatible with readily available audio transducers.

In addition to its many purpose audio applications, the WC 183 amplifier performs unusually well in battery powered applications, and in particular as a hearing aid amplifier. In this application, its high gain at low voltage, low quiescent current drain and highly efficient class B output stage result in improved sound quality and significant increased battery life.

Design features

- 90 db gain at V_{CC} of 4.5 volts
- 60 db minimum gain at V_{CC} of 1.55 volts
- Quiescent current less than 1 ma
- Efficiency 55%

Reliability assurance

Mechanical and environmental reliability assurance EVERY unit receives

- High temperature storage bake at $+150^{\circ}\text{C}$
- 20,000 G centrifuge
- Hermeticity tests
- Failure rate is less than 0.01% per 1000 hours from current life test data

**Absolute maximum ratings**

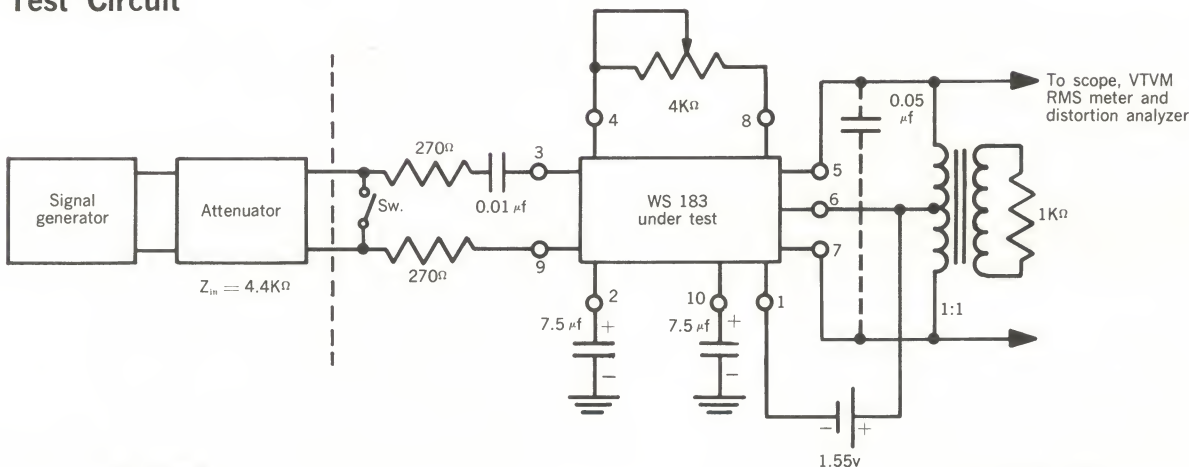
Parameter	Symbol	Value	Units
Power supply voltage	V_{cc}	9	volts
Power dissipation	P_D	100	mw
Storage temperature	T_{stg}	-65°C to +125°C	°C
Operating temperature	T_{opg}	-55°C to +75°C	°C

Thermal impedance

Conditions	Value	Units
Free air at 25°C	265	°C per watt
Infinite heat sink at 25°C	35	°C per watt

Typical electrical characteristics(at 25°C and $P_{out} = 3.0\text{mw}$ unless stated otherwise)

Parameter	$V_{cc} = 1.55\text{ volts}$			$V_{cc} = 4.5\text{ volts}$			Units
	Min.	Typ.	Max.	Min.	Typ.	Max.	
Power output	3.0			35	45		mw
Voltage gain (pins 4 and 8 shorted)	60	72		84	94		db
Battery current		4.3	5.0		5.2	9.0	ma
Quiescent current		0.9	1.5		3.5	8.0	ma
Total harmonic distortion		6	8		7	9	%
Noise voltage at output *		3	10		3	12	mv
Frequency response (3 db down points)							
Uncompensated	12,000		300	10,000		500	Hz
With .05 μf rolloff capacitor between pins 5 and 7	4,200		300	3,000		500	Hz
Input impedance		40			40		K Ω
Volume control range	25	35		25	52		db
Efficiency		55		45	68		%

* Gain $\leq 72\text{ db}$ **Test Circuit****Figure 3**

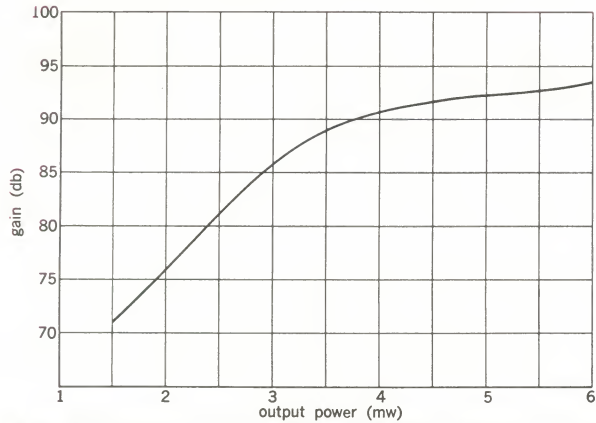
Pin numbers refer to G package



Typical electrical characteristics

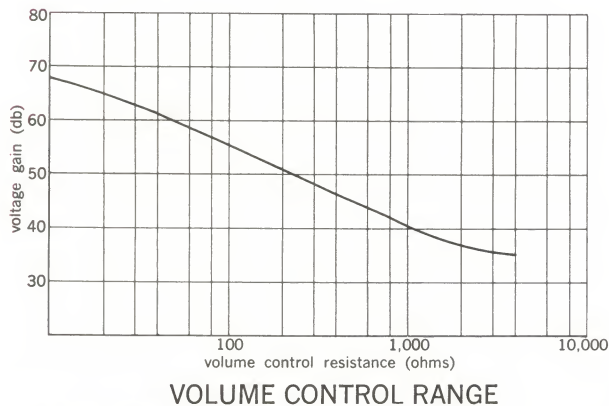
(at 25°C and at $V_{cc} = 1.55\text{v}$ and 3.0mw unless stated otherwise)

(Refer to Fig. 3 for test circuit)



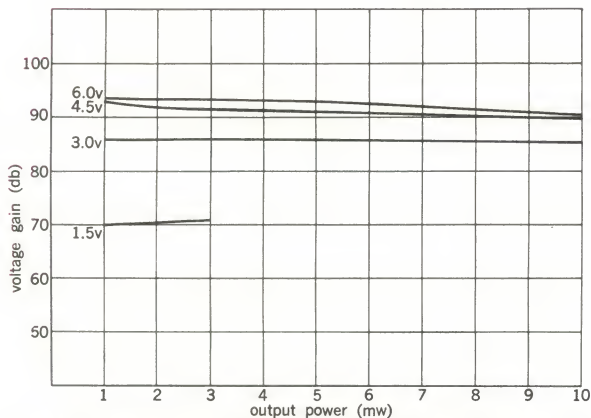
GAIN VS SUPPLY VOLTAGE AT 3 MW POWER OUTPUT

Figure 4



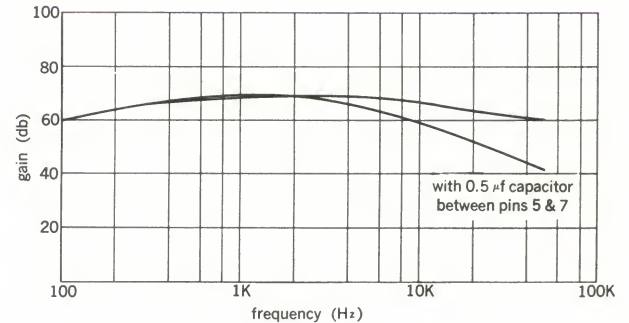
VOLUME CONTROL RANGE

Figure 5



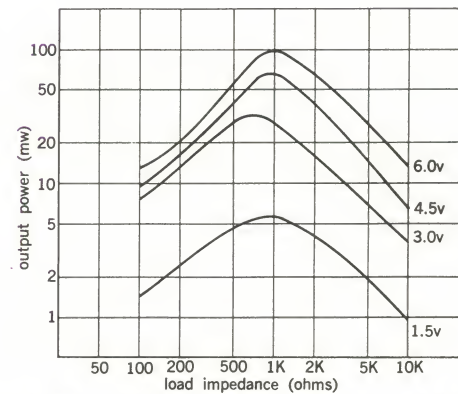
GAIN VS POWER OUTPUT AS A FUNCTION OF V_{cc}

Figure 6



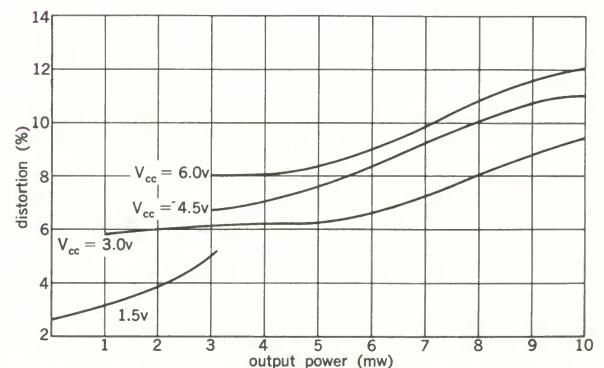
FREQUENCY RESPONSE CHARACTERISTIC

Figure 7



MAXIMUM OUTPUT POWER VS LOAD IMPEDANCE

Figure 8



TOTAL HARMONIC DISTORTION VS POWER OUTPUT

Figure 9



Typical applications

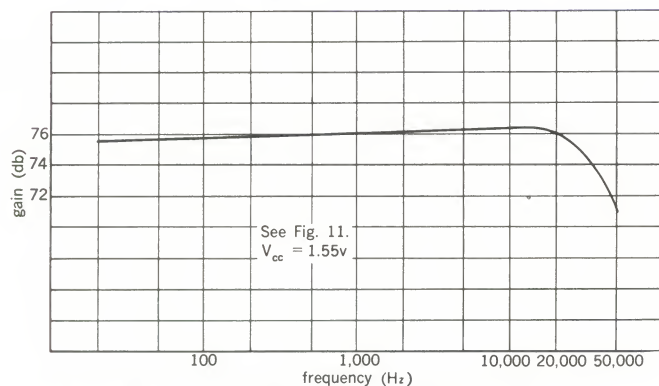


Figure 10 FREQUENCY RESPONSE

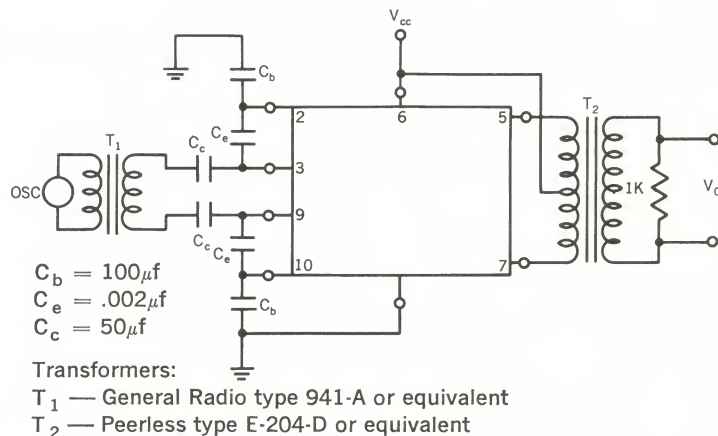


Figure 11

WIDE BAND AMPLIFIER

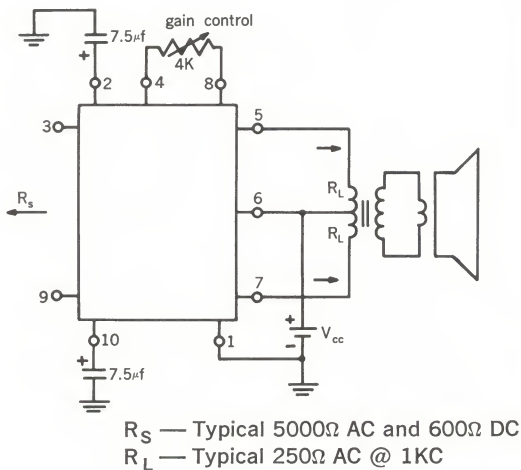


Figure 12

AUDIO AMPLIFIER

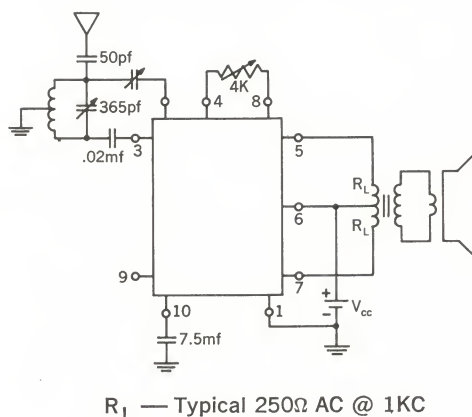


Figure 13

BROADCAST BAND REGENERATIVE RECEIVER

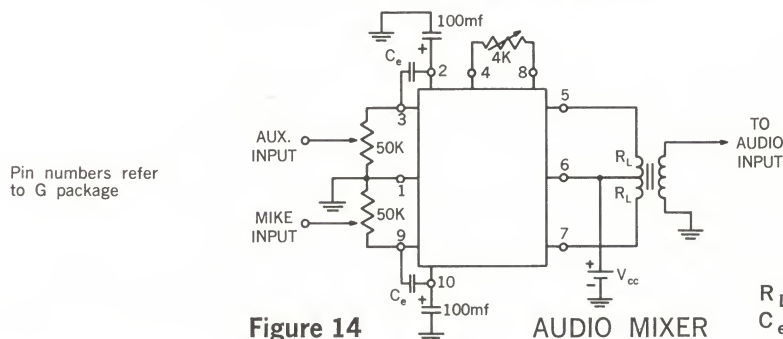


Figure 14

AUDIO MIXER

References:

Hellstrom, M. J., and Hsieh, J. J. "A Monolithic Silicon Class B Hearing Aid Amplifier." WESCON, San Francisco, 1965.
 Hellstrom, M. J. "A Family of Integrated Class B Hearing Aid Circuits." National Electronics Conference, Chicago, 1965.

All values shown subject to design change for product improvement.

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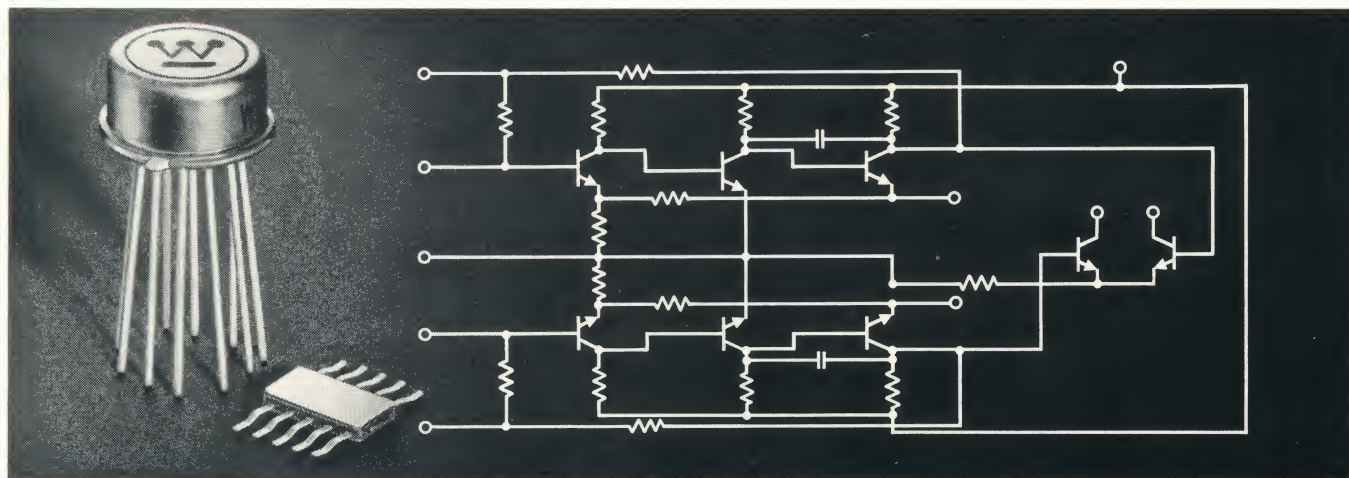
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